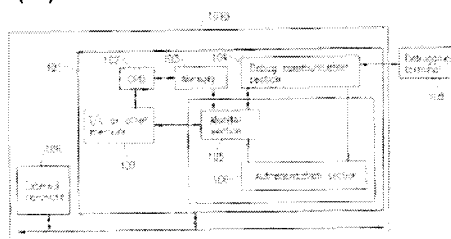


**Information processing device****Publication number:** CN1383070 (A)**Publication date:** 2002-12-04**Inventor(s):** HIROYUKI UDONO [JP]; TAKAHIRO SATO [JP]; SEI USUI [JP]**Applicant(s):** MATSUSHITA ELECTRIC IND CO LTD [JP]**Classification:****- international:** G06F9/44; G06F11/22; G06F12/14; G06F21/00; G06F9/44; G06F11/22; G06F12/14; G06F21/00; (IPC1-7): G06F12/14; G06F11/22**- European:** G06F21/00N1V3**Application number:** CN20021018082 20020422**Priority number(s):** JP20010122435 20010420**Also published as:**CN1181436 (C)  
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An information processing apparatus configured so as to be connectable to a debugging terminal includes: a CPU for executing a program; a monitor section for monitoring an execution status of the program; a debug communication section configured so as to be connectable to the debugging terminal; and an authentication section connected to the monitor section and the debug communication section, wherein the debug communication section receives debug information from the debugging terminal, the authentication section determines whether or not the monitor section is allowed to output monitor information corresponding to the debug information, according to whether the authentication section is in an authenticated state or in an unauthenticated state, and the debug communication section transmits to the debugging terminal the monitor information output from the monitor section.

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